

### **AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application:

#### **Listing of Claims:**

1-35 (canceled)

36. (Currently amended): A thin film transistor substrate comprising:  
an insulating substrate;  
a first signal line including a gate electrode and formed on the insulating substrate;  
a second signal line formed on the insulating substrate;  
a gate insulating layer covering the first and the second signal lines;  
a semiconductor layer formed on the gate insulating layer;  
a third signal line ~~intersecting~~ overlapping the first and the second signal lines;  
a drain electrode and a source electrode formed on the semiconductor layer, wherein the third signal line is connected to the source electrode;  
a protective layer having a contact hole exposing the drain electrode, wherein the contact hole is located over the second signal line; and  
a pixel electrode connected to the drain electrode through the contact hole,  
wherein the first and second signal lines are adapted to receive a same gate signal:

37. (previously presented): The thin film resistor of claim 36, wherein the pixel electrode overlaps the first and the second signal lines.

38. (previously presented): The thin film resistor of claim 36, wherein the drain electrode is extended over the second signal line.

39. (previously presented): The thin film resistor of claim 36, further comprising a connection portion connected to the second signal line.

40. (previously presented): The thin film resistor of claim 39, wherein the pixel electrode overlaps the connection portion.

41. (previously presented): The thin film resistor of claim 40, wherein the connection portion overlaps the third signal line.